



## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Confirmation No. 5610

**Erwin HEMMING** Group Art Unit: 2183

Examiner: Unassigned Application No.: 10/611,919

Attorney Dkt. No.: 47092.00032 Filed: July 3, 2003

For: INTERLEAVING METHOD AND APPARATUS WITH PARALLEL ACCESS

IN LINEAR AND INTERLEAVED ORDER

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

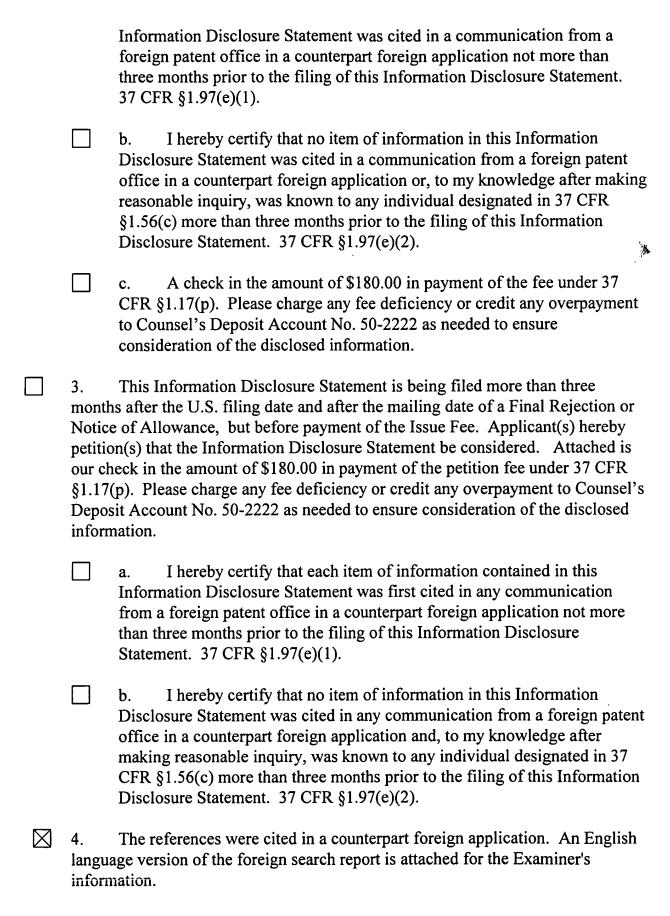
September 17, 2004

Sir:

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Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the information item(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each item(s) is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the item(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date or the date of filing a CPA, OR (b) before the mailing date of a first Office Action on the merits in the present application, or (accompanies a Request for Continued Examination. No certification or fee is required.							
2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.							
a. I hereby certify that each item of information contained in this							



In the event that there are any fees due with respect to the filing of this paper, please charge Counsel's Deposit Account No. 50-2222.

Respectfully submitted,

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Enclosures: PTO-1449 Form; 4 References; European Search Report

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FORM PTO	)-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK-QEFICE			ATTY. DOCKET NO.			SERIA	SERIAL NO.			
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)  AL Giulietti, et al.; "Parallel Turbo Coding Interleavers: Avoiding Collisions in Accesses to Storage Elements"; ELECTRONICS LETTERS, Vol. 38, No. 5, February 28, 2002, pp. 232-234												
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	AN	Richter, et al; "Parallel Interleaving on Parallel DSP Architectures"; IEEE, October 16, 2002, pp. 195-200										
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